Sunil Nanda
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Experience

2010 – Till date
Founder, Evangelist – Pilani AtmaNirbhar Resource Center (PARC)
Conceived, conceptualized, planned, built, recruited for, forged partnerships for and managed PARC – a social incubator in Pilani dedicated to community development of Pilani and its surroundings. Housed in a 17000 sq. ft. building built specifically for this purpose, PARC has been serving the community for the past decade with a plethora of interventions that range from vocational training, science education, computer literacy, physiotherapy for seniors, multiple health camps, water conservation and organic pesticide initiatives and more. Today we can boast of having secured employment for 1000+ underprivileged boys and girls, provided health care to 3000+ local members of the community, helped 100+ with computer literacy, imparted hand-on science education to 20,000+ school children via Mobile Science Labs etc. Our website https://atmanirbhar.org provides a lot more detail.

2010 – Till date
Independent Technology Investor
As an angel investor for startups, provide managerial and technical expertise and work on product strategy for these companies. Essential role is to nurture these companies from a nascent to a mature stage.

Feb 2004 – May 2009
Senior Director and India Founder, Nvidia Inc.
Founded and Setup a VLSI design Center in India from scratch. Currently centers in Bangalore, Pune and Hyderabad. Total number of engineers nearing 2000. Activities cover the entire spectrum of ASIC design from architecture, RTL, verification, synthesis, timing, emulation, silicon bring-up and characterization in various technologies. Won about 100 awards worldwide for nForce680i chipset that was designed completely out of Bangalore.

Feb 2000-Dec 2002
GM, Ethernet Switching Operation, Intel corporation.
Managed all engineering on chips for Ethernet Switching for Intel. Responsible for all areas of chip design from architecture to production across multiple engineering sites in San Jose, Jerusalem, and Bangalore. Worked with Fabs in Taiwan and Intel's internal Fabs on process technologies. Served as General Manager of the operation with P&L responsibilities.

1998-Jan 2000
Founder, Director, Thinkit Technologies Inc.
Co-founded the company and built a crack engineering team in Bangalore as part of a startup company. Executed a number of projects in the Ethernet switching domain. Thinkit was acquired by Intel in Feb' 2000 – the first company acquired by Intel in South Asia. Thinkit's VLSI team formed the core of the Intel VLSI team in Bangalore.

1996-1998
S3 Fellow & Director of Engineering, S3 Inc.
Established a design center for S3 in Bangalore from ground zero. Established new design methodologies for multi-million gate designs for S3. Also included design of SMOP (a unique, patented comms/audio DSP) and design management for Savage3D (S3's first 1M+ gate ASIC and flagship product at that time). Relocated to India from USA during this time.

1992-1996
Architect, Redwood & Cadence Design Systems
Architected cycle-based simulators for VHDL and VERILOG. Led a team for the implementation with multiple innovative and groundbreaking concepts for fast simulation. Architected a dataflow scheduler, simulator, and code generation system for DSP systems. The system was in production in record time and a major revenue earner for ALTA (a subsidiary of Cadence). As part of the founding team of Redwood Design, was instrumental in its merger with Cadence Design Systems.

1988-1992
Design Manager, SUN Microsystems
Project Lead for a Floating-point processor for SPARC as an ASIC. Done in record time of 4 months. Pioneered the use of Synopsys synthesis tools within SUN. Chief Architect and lead designer for the Integer unit and Memory Management Unit for the first superscalar SuperSparc processor. Instrumental in setting CAD strategy for the entire project.

1987-1988
Principal Engineer, Data General
Conceived, evangelized and led a team to design DG's first Hardware Design Language (prior to the advent of Verilog etc.). Also included a compiler for the HDL and a mixed-level digital simulator.

1982-1987
Senior Engineer, Intel Corporation
Lead Designer of the Graphics Processor for P6 project. Included architecture, logic design and microcode. Instrumental in developing INTEL's first RTL simulator USIM82. Lot of design and coding in MAINSAIL. Recognized as an expert in new CAD Tool generation.

Patents

US06044448
Method and apparatus for Sliceability of a Media Operations Processor.

US05958038
Method and apparatus for Data Streaming in a Media Operations Processor

US05509130
Method and apparatus for grouping multiple instructions, issuing grouped instructions simultaneously, and executing grouped instructions in a pipelined processor

US05497480
Broadcast demap for deallocating memory pages in a multiprocessor system

US05390190
Inter-domain latch for scan based design

US05329627
Method and apparatus for a translation lookaside buffer with built-in replacement scheme in a computer system

US04797852
Block shifter for graphics processor

Education
B.E (Electronics) 1980 BITS Pilani – GPA 9.6 / 10.0
M.S (Electrical and Computer Engg) 1982 Univ. Of Iowa – GPA 4.0 / 4.0

Profile

I graduated from BITS, Pilani in 1980 with a BE-Electronics followed by an MS-Computer Engineering from University Of Iowa, USA. I spent the first 15 years of my career in the USA before relocating back to India.

As a recognized industry expert in Computer Architecture and VLSI design, I have multiple technical patents to my credit.

As an entrepreneur, I founded and successfully led Thinkit Technologies till it's acquisition by Intel Corp. I also founded the India Design Center for Nvidia and grew it from zero to a thriving engineering center churning out award winning VLSI designs. Both of these roles required building technical powerhouses starting from ground zero.

After three decades in technical & senior management roles spanning both the USA and India, I switched from the corporate sector to give back to the society through novel social initiatives.

As a key member of BITSAA Alumni Trust founded by my batch, I have been spearheading the Pilani AtmaNirbhar Resource Center (PARC) project in Pilani since 2010, where I manage a collection of social intervention initiatives for the underprivileged. Under my leadership, PARC has evolved from an abstract idea on paper to a humming beehive of activities with its own brick-and-mortar center catering to the needs of local community in and around Pilani, Rajasthan. Today PARC is a well-respected social service institution in Pilani.

A person with varied interests, I actively spend my time on social initiatives, music, math evangelizing and as an Angel Investor for startup companies. I have been continually active within the BITS Alumni community, be it delivering lectures at BITS, mentoring students, working with professors, etc. With utmost humility, I accepted the LKM Distinguished Alumni award in Electronics by BITS, Pilani in 2017 and the BITS Distinguished Alumnus Award in Social Entrenepeurship in 2020.